

Unlocking the power of JTAG

This pinout is fully compatible with the ARM Multi-ICE 20-pin header (2.54mm pitch).

All GND signals (ground) should be connected on your target board for improving the integrity of the JTAG signals.

The GND signals are on pin: 4 6 8 10 12 14 16 18 20.



5V to 1.4V
IOs
TOLERANT

p : power
i : input
o : output
io: input/output

GND	p	20	19		n.c.
* (GND)	p	18	17		n.c.
* (GND)	p	16	15	io	SRST_N
* (GND)	p	14	13	i	TDO
* (GND)	p	12	11		n.c.
* (GND)	p	10	9	o	TCK
* (GND)	p	8	7	o	TMS
* (GND)	p	6	5	o	TDI
* (GND)	p	4	3	o	TRST_N
* (VREF)	p	2	1	p	VREF

NOTE: For understanding the use of the *(GND) signals and *(VREF) signal, please make sure to read the datasheet of the Amontec JTAGkey.